

## 5 bandstop

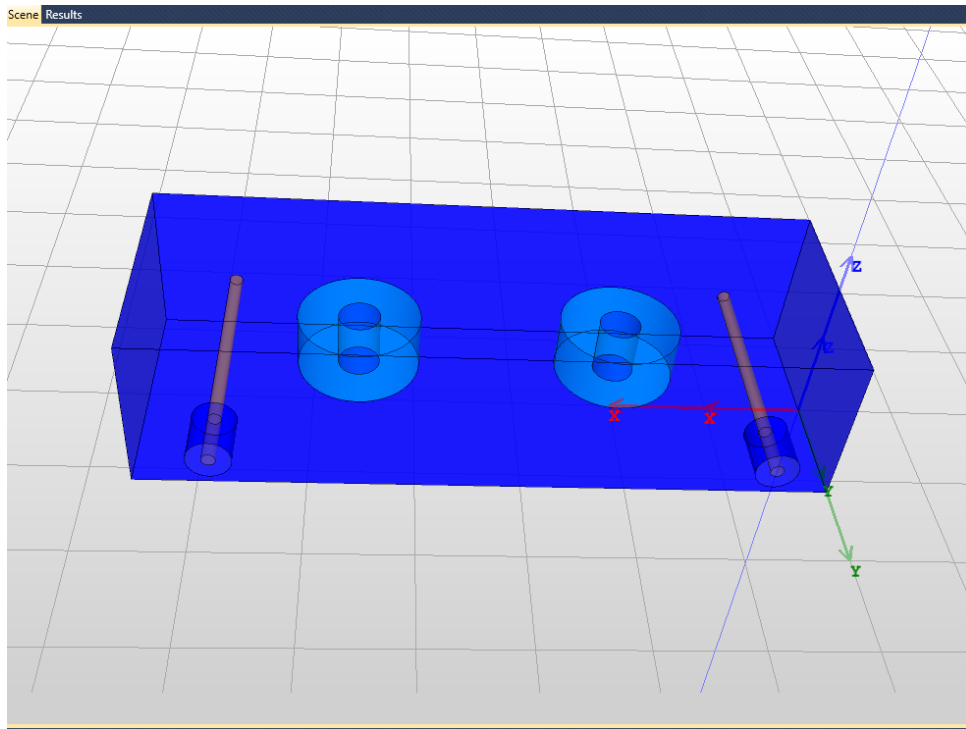
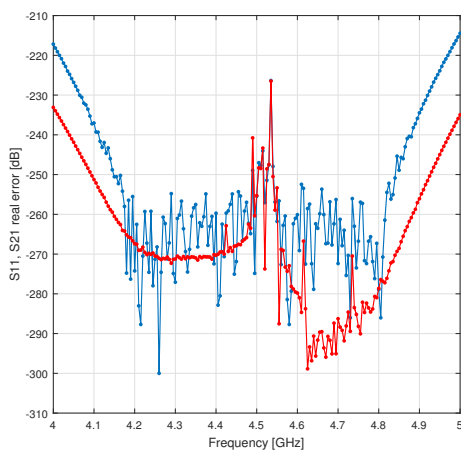
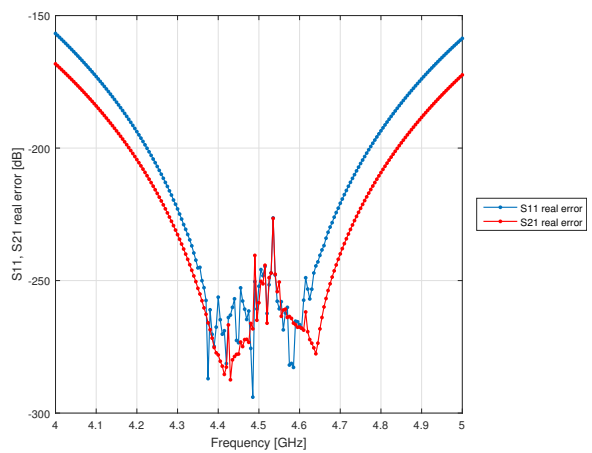


Figure 13: Screenshot of the structure drawn in InventSim

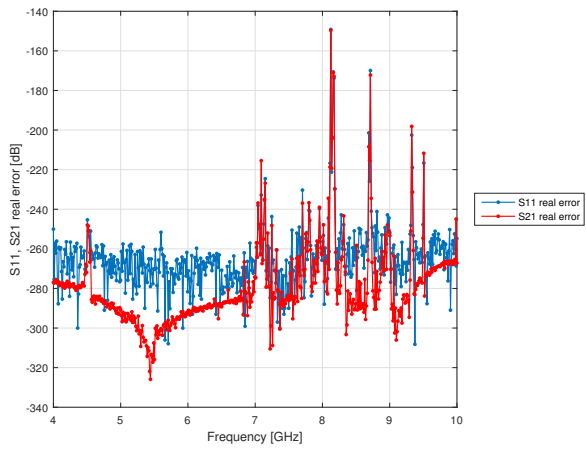


(a) new solver

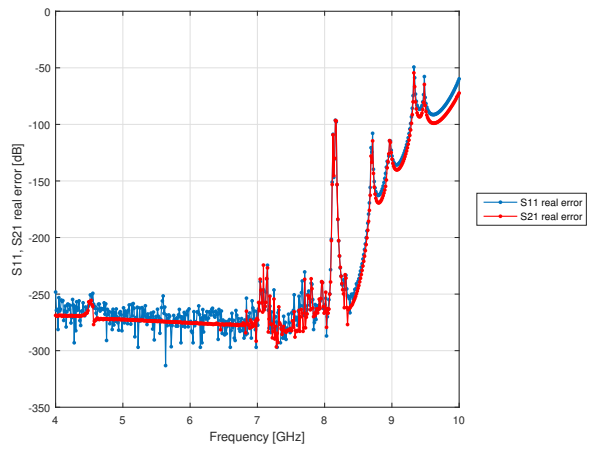


(b) old solver

Figure 14: Error rate for bandwidth working



(a) new solver



(b) old solver

Figure 15: Error rate for wide band

## 6 comb4

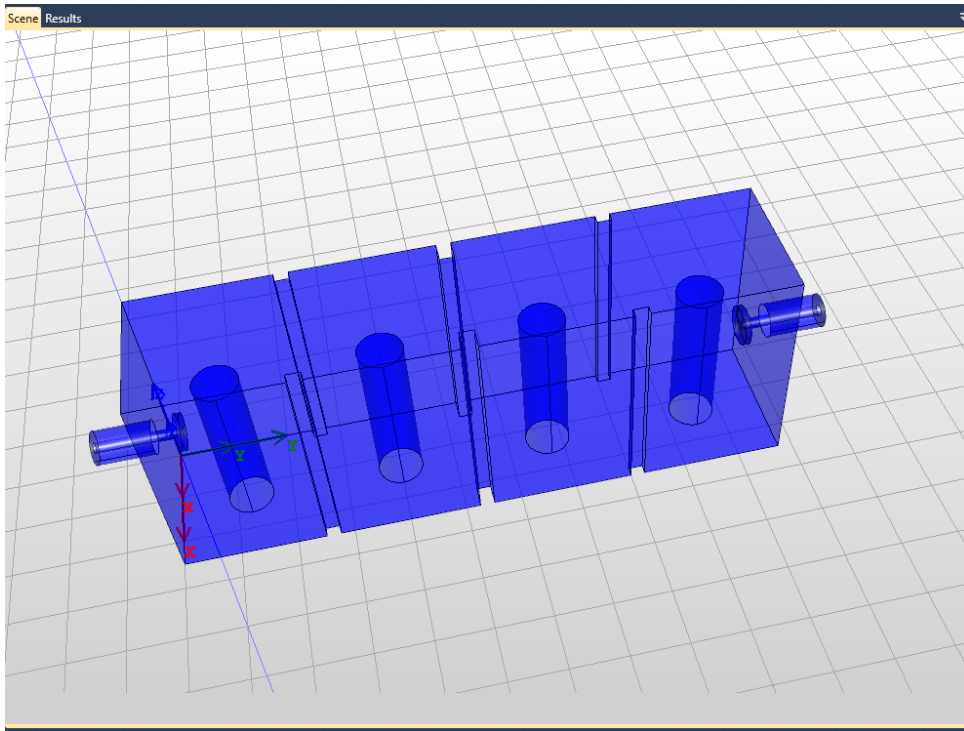
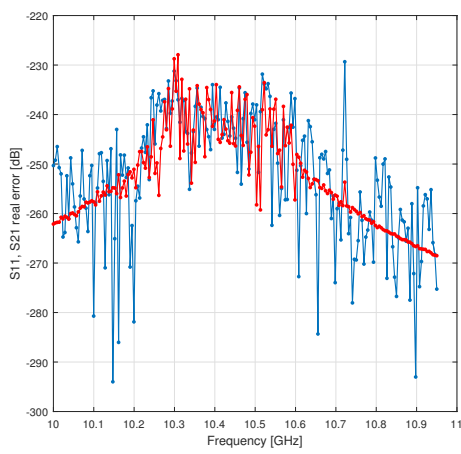
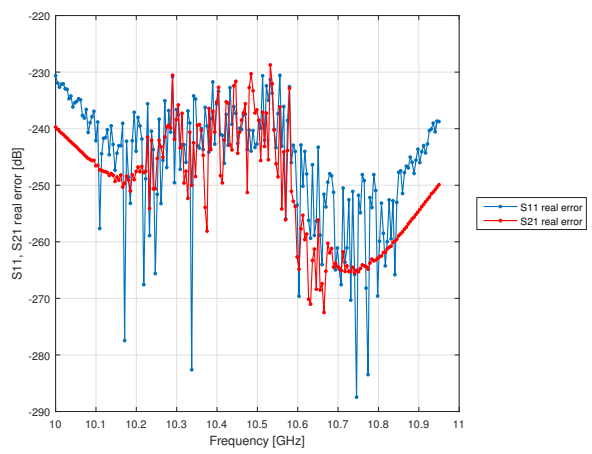


Figure 16: Screenshot of the structure drawn in InventSim

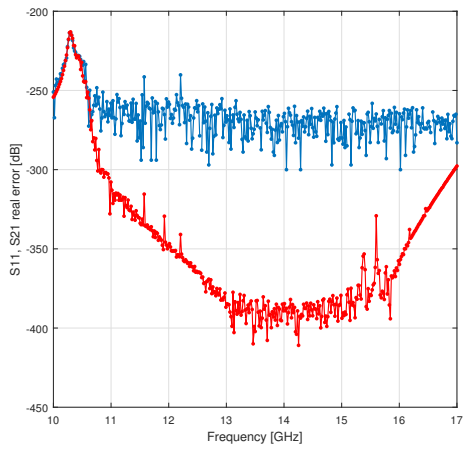


(a) new solver

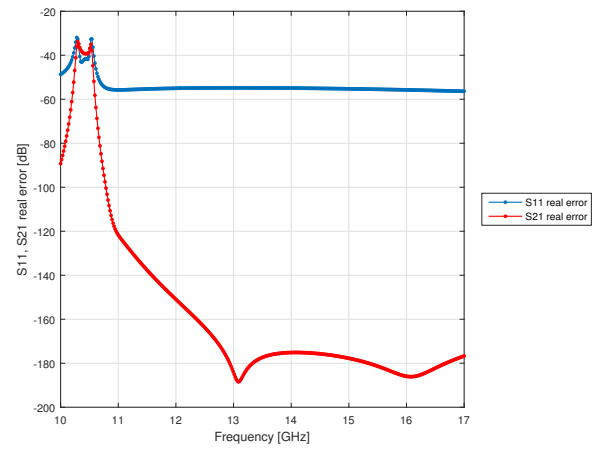


(b) old solver

Figure 17: Error rate for bandwidth working



(a) new solver



(b) old solver

Figure 18: Error rate for wide band

## 7 comb6 duplex with hole finall

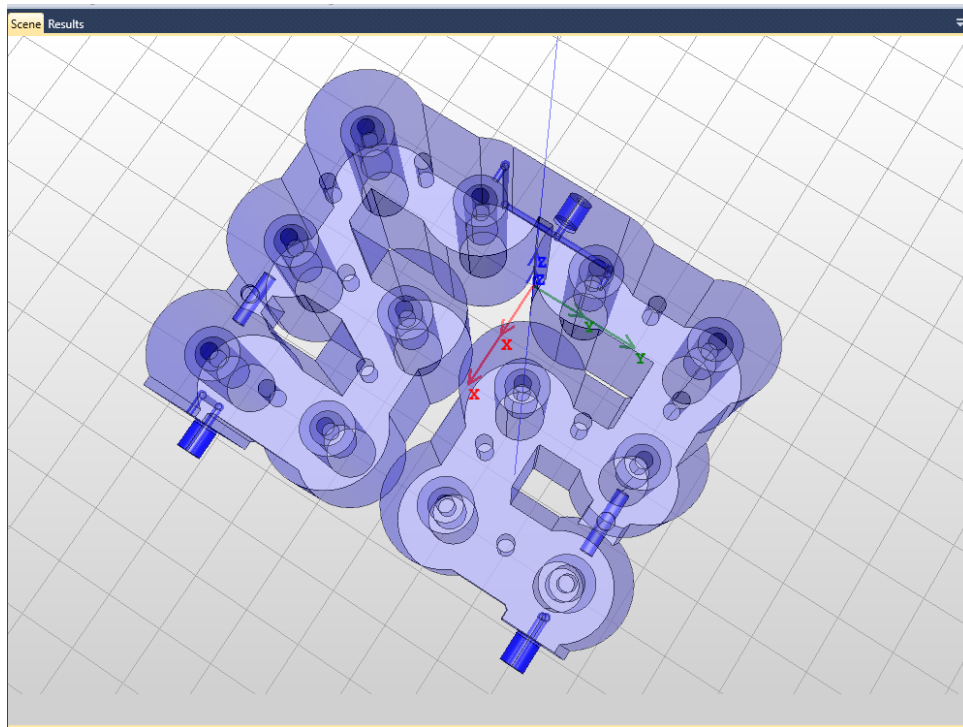
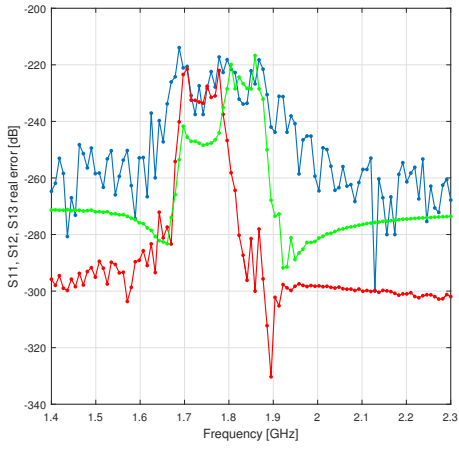
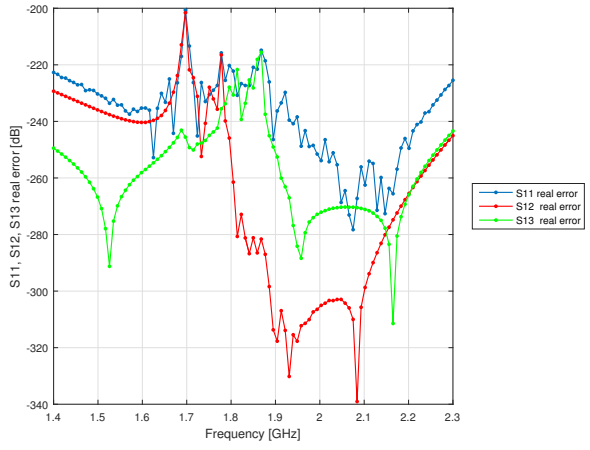


Figure 19: Screenshot of the structure drawn in InventSim

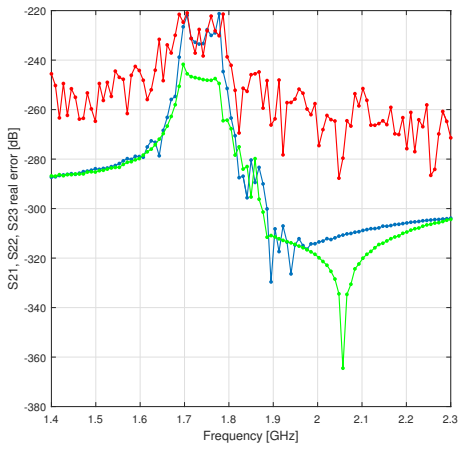


(a) new solver

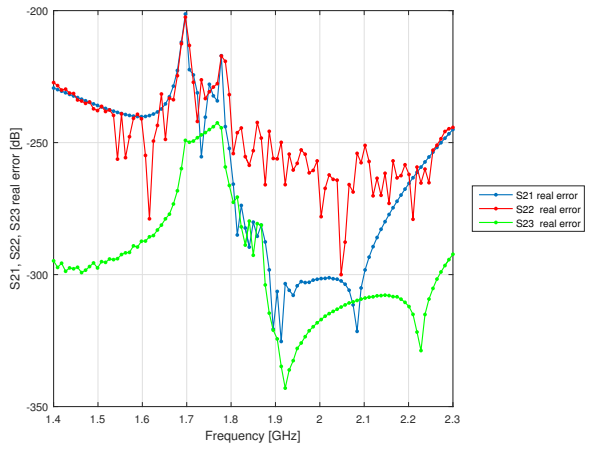


(b) old solver

Figure 20: Error rate for bandwidth working

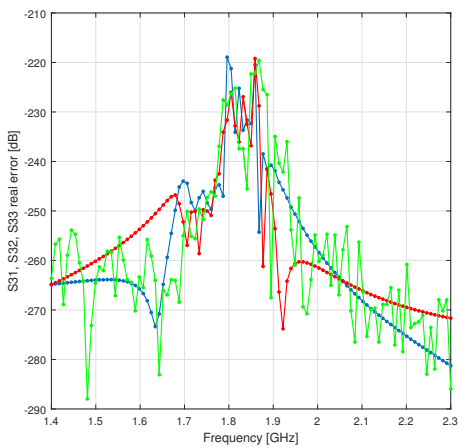


(a) new solver

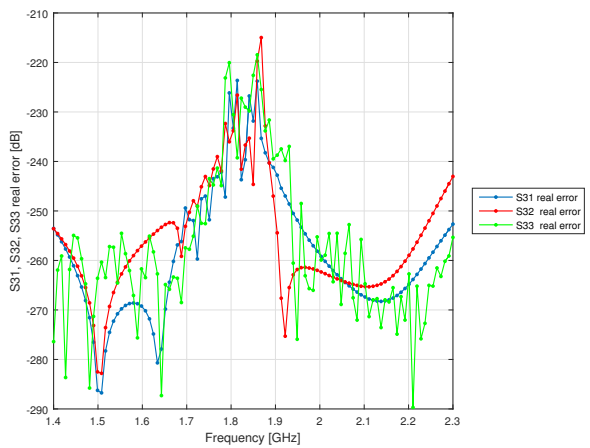


(b) old solver

Figure 21: Error rate for bandwidth working

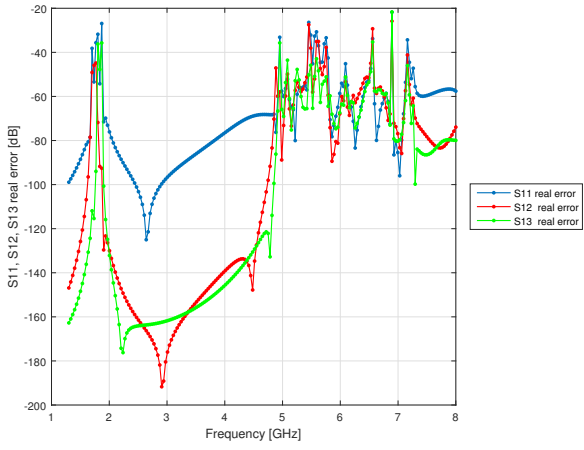


(a) new solver

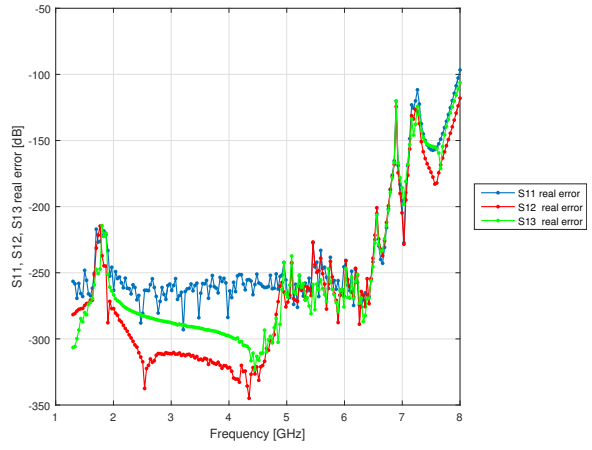


(b) old solver

Figure 22: Error rate for bandwidth working

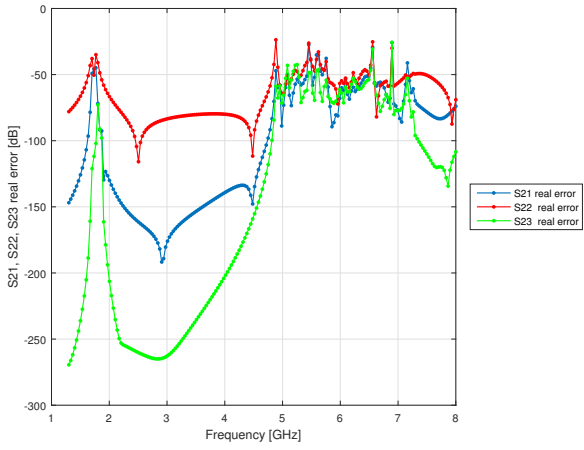


(a) new solver - too big error

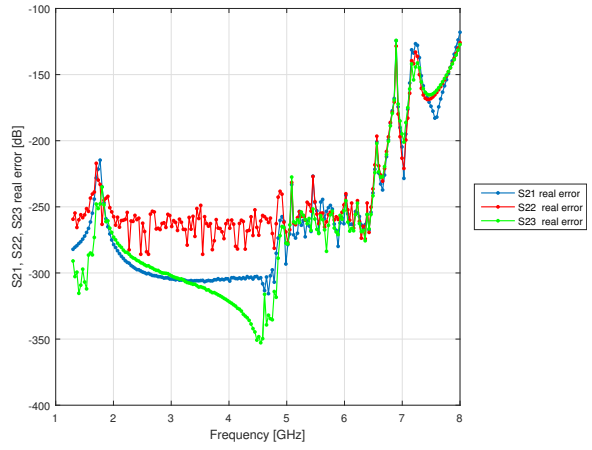


(b) old solver

Figure 23: Error rate for wide band

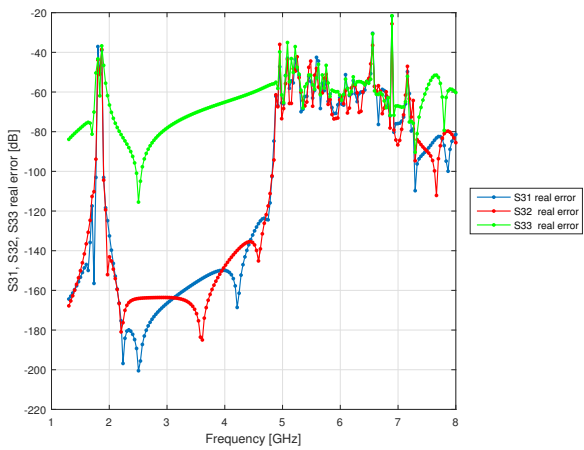


(a) new solver - too big error

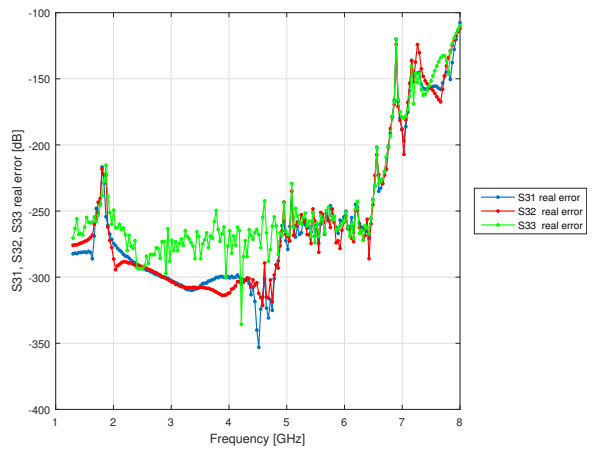


(b) old solver

Figure 24: Error rate for wide band



(a) new solver - too big error



(b) old solver

Figure 25: Error rate for wide band